

a mixer stage having inputs coupled to the outputs of said controllable oscillator and said divider and producing an output. used in generating a signal at the transmit frequency.

17. (NEW) The electronic circuit as claimed in claim 16, further comprising a band filter, coupled to the output of said mixer stage, to generate the signal at the transmit frequency.

18. (NEW) The electronic circuit as claimed in claim 17, further comprising a phase locked loop circuit coupled to an input of said controllable oscillator to provide a reference frequency and to receive as an input at least one of the output of said controllable oscillator and the signal at the transmit frequency produced by the band filter.

19. (NEW) The electronic circuit as claimed in claim 17, further comprising  
a transmit output stage coupled to receive the signal at the transmit frequency from said band filter; and

a control device, coupled to the output of said mixer stage when said transmit output stage is switched on, to superimpose on an oscillator control signal a data signal to generate a frequency modulation of the output of said controllable oscillator.

20. (NEW) The electronic circuit as claimed in claim 16, wherein said mixer stage comprises a single-sideband mixer.

21. (NEW) The electronic circuit as claimed in claim 20, wherein said single-sideband mixer is an Image Reject Mixer.

22. (NEW) The electronic circuit as claimed in claim 20, further comprising a phase locked loop circuit coupled to an input of said controllable oscillator to provide a reference frequency and to receive as an input at least one of the output of said controllable oscillator and the output of said single-sideband mixer.

23. (NEW) The electronic circuit as claimed in claim 22, further comprising  
a transmit output stage coupled to receive the signal at the transmit frequency from said single-sideband mixer; and

a control device, coupled to the output of said single-sideband mixer when said transmit output stage is switched on, to superimpose on an oscillator control signal a data signal to generate a frequency modulation of the output of said controllable oscillator.

24. (NEW) The electronic circuit as claimed in claim 23, wherein said control device is an ASIC component.

25. (NEW) The electronic circuit as claimed in claim 23, wherein said control device activates two switches alternately, to disconnect the control input of the oscillator upon switching on said transmit stage by said phase locked loop circuit and to supply the data signal for frequency modulation.

26. (NEW) The electronic circuit as claimed in claim 25, further comprising  
a superimposition receiver, coupled to the output of said controllable oscillator to obtain a superimposition frequency directly from the oscillator frequency; and

a switch circuit having a first input used during transmission coupled to the output of said mixer stage, a second input used during reception coupled to said controllable oscillator, and an output coupled to said phase locked loop circuit.

27. (NEW) The electronic circuit as claimed in claim 16, further comprising an amplifier having an input coupled to the output of said mixer stage.

28. (NEW) The electronic circuit as claimed in claim 16, wherein said controllable oscillator is voltage-controlled.

29. (NEW) The electronic circuit as claimed in claim 16, wherein said controllable oscillator is current-controlled.

30. (NEW) The electronic circuit as claimed in claim 16, wherein a reference frequency is supplied externally.

31. (NEW) The electronic circuit as claimed in claim 16, further comprising a modulator, coupled between said divider and said mixer stage, to supply an IQ modulation baseband signal.

32. (NEW) The electronic circuit as claimed in claim 31, wherein said modulator performs vector modulation.

33. (NEW) The electronic circuit as claimed in the preceding claim 32, wherein the output from said divider, phase-shifted by  $0^\circ/90^\circ$ , is used in generation of the vector modulation of said modulator.

34. (NEW) The electronic circuit as claimed in claim 16, further comprising a modulation stage at an output of said electronic circuit to perform modulation of the transmit signal.

35. (NEW) The electronic circuit as claimed in claim 31, wherein said modulation stage is a vector modulation stage.

#### **IN THE ABSTRACT:**

Please DELETE the Abstract in its entirety and replace with the attached Substitute Abstract.

#### **REMARKS**

This Preliminary Amendment is submitted to improve the form of the English translation as filed. It is respectfully requested that this Preliminary Amendment be entered in the above-referenced application.

In accordance with the foregoing, claims 1-15 have been canceled and claims 16-35 have been added. Thus, claims 16-35 are pending and are under consideration.

A substitute specification is also being filed herewith. The substitute specification is accompanied by a marked-up copy of the original specification.

If there are any questions regarding these matters, such questions can be addressed by telephone to the undersigned. Otherwise, an early action on the merits is respectfully solicited.